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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/699,791	11/04/2003	Cheol-Soo Jung	6192.0128.C3	4021

7590 11/17/2006

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EXAMINER

LIU, BENJAMIN T

ART UNIT PAPER NUMBER

2826

DATE MAILED: 11/17/2006

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

Application No.

10/699,791

Applicant(s)

JUNG ET AL.

Examiner

Benjamin T. Liu

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 19 September 2006.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 21-24 and 31 is/are pending in the application.
- 4a) Of the above claim(s) 1-20 and 25-30 is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 21-24 and 31 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

THOMAS DICKEY
PRIMARY PATENT EXAMINER

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on _____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
- Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
- Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☒ All b) ☐ Some * c) ☐ None of:
1. ☒ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. _____.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- * See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- 1) ☒ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☒ Information Disclosure Statement(s) (PTO/SB/08)
Paper No(s)/Mail Date 11/4/03.
- 4) ☐ Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____.
- 5) ☐ Notice of Informal Patent Application
- 6) ☐ Other: _____.

DETAILED ACTION

1. Claims 1-20 and 25-30 canceled in amendment submitted 9/19/06

Oath/Declaration

2. The oath/declaration filed on 11/4/03 is acceptable.

Drawings

1. The formal drawings filed on 11/4/03 are acceptable.

Priority

2. Receipt is acknowledged of papers submitted under 35 U.S.C. 119(a)-(d), which papers have been placed of record in the file.

Information Disclosure Statement

3. The Information Disclosure Statement filed on 11/4/03 has been considered.

Claim Rejections - 35 USC § 102(e)

4. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States

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only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

Claims 21-24 and 31 are rejected under 35 U.S.C 102(e) as being anticipated by Seo et al. (6,445,435).

With regard to claim 21, figure 2A of Seo et al. discloses a thin film transistor (TFT) array panel for a liquid crystal display, comprising: a first line 102; a second line 101 intersecting the first line 102; a pixel region ("pixel region", line 54 of Seo et al.) defined by the intersection of the second line 101 and the first line 102; a pixel electrode formed 108 in the pixel region ("pixel region", line 54 of Seo et al.); and, a ladder-shaped conductive pattern (103 above and below the TFT 105, 109 to the left and right of the TFT 105) connected to the second line 101 and having a pair of parallel spaced-apart riser-like elements (109 to the left and right of TFT 105) respectively disposed on opposite sides of the first line 102.

With regard to claim 22, figure 2A of Seo et al. discloses a storage wire 109 including a storage electrode line (103 below the TFT 105 running parallel to the gate line 101).

With regard to claim 23, figures 2A and 2B of Seo et al. disclose a passivation layer 120 formed between the pixel electrode 107 and the first line 102 and the second line 101.

With regard to claim 24, figure 2A of Seo et al. discloses a thin film transistor (TFT) array panel for a liquid crystal display, comprising: a first line 102; a common wire 109 including a common electrode line 103; a second line 101 intersecting the first line 102; a pixel region ("pixel region", line 54 col 3 of Seo et al.) defined by the intersection

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of the first line 102 and the second line 101; a pixel electrode 108 formed in the pixel region ("pixel region", line 54 col 3 of Seo et al.) and overlapping the common electrode 103; and, a ladder-shaped conductive pattern (103, 109) connected to the second line 101 and having a pair of parallel, spaced-apart riser-like elements 109 respectively disposed on opposite sides of the first line 102.

With regard to claim 31, figure 2A of Seo et al. discloses the conductive pattern 109 and the storage wire 103 are formed in the same layer.

5. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Benjamin T. Liu whose telephone number is (571) 272-6009. The examiner can normally be reached on Mon-Fri 9:30 AM-6:00AM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Nathan Flynn can be reached on 571 272 1915. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

BTL

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11/12/2006